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Commissioner for Patents
Washington, D.C. 20231


E.L. Garske
Attorney for Applicants

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TECHNOLOGY CENTER
AUG 22 2001
PATENT
Attorney Docket No.: DB000575-0280



Date: 30 July 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keeth, et al.)
Serial No.: 09/934,795)
Filing Date: 22 August 2001)
Title: 256 MEG DYNAMIC RANDOM ACCESS MEMORY

TRANSMITTAL LETTER

To: Commissioner for Patents
United States Patent and Trademark Office
Office of Initial Patent Examination
Customer Service Center
Washington, DC 20231

Dear Sir:

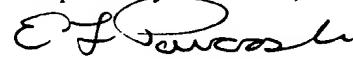
Enclosed for filing in the above-captioned application, please find the following:

1. a Second Request for Corrected Filing Receipt;
2. a copy of the Preliminary Amendment;
3. a copy of the original Filing Receipt with corrections marked;
4. a copy of the first Request for Corrected Filing Receipt;
5. a copy of the Second Preliminary Amendment; and
6. a copy of the Updated Filing Receipt with corrections marked in red ink.

Also enclosed is a return postcard. Please date stamp and mail the postcard in order to acknowledge receipt of the above-reference correspondence.

The Commissioner is hereby authorized to charge any underpayment or credit any overpayment to our Deposit Account No. 20-0888. A duplicate copy of this transmittal is enclosed.

Respectfully submitted,



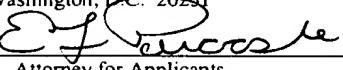
Edward L. Pencoske
Reg. No. 29,688
THORP REED & ARMSTRONG, LLP
One Oxford Centre, 14th Floor
Pittsburgh, PA 15219-1425
(412) 394-7789

Dated: 30 July 2002

Attorneys for Applicant

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Washington, DC 20231


Edward L. Pencoske
Attorney for Applicants

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Dated: 30 July 2002

PATENT
Attorney Docket No.: DB000575-023

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AUG 22 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Keeth, et al.)	Examiner:	Not yet assigned
Serial No.:	09/934,795)	Art Unit:	2816
Filing Date:	22 August 2001)		
Title:	256 MEG DYNAMIC RANDOM ACCESS MEMORY			

SECOND REQUEST FOR CORRECTED FILING RECEIPT

To: Commissioner for Patents
United States Patent and Trademark Office
Office of Initial Patent Examination
Customer Service Center
Washington, DC 20231

Dear Sir:

Applicants hereby request a second Corrected Filing Receipt be issued for the above-identified patent application. The Patent Office has incorrectly listed the Applicants for this invention.

Applicants respectfully request that the Applicants be listed to include only Brent Keeth and Layne G. Bunker. The Statement Requesting Deletion of Inventors (copy enclosed), which was included in the Second Preliminary Amendment filed on 07 November 2001 requested that the above-identified Application be filed only in the names of Keeth and Bunker.

A copy of the Updated Filing Receipt with corrections marked in red ink is enclosed. Additionally, copies of the original Preliminary Amendment and the first Request for Corrected Filing Receipt are enclose to evidence Applicants' attempts to diligently obtain a Filing Receipt reflecting the correct Applicants.

Respectfully submitted,



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Attorneys for Applicant

Dated: 30 July 2002



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DATE OF DEPOSIT: August 22, 2001

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Patents, Washington, DC 20231

THORP REED AND ARMSTRONG, LLP
One Oxford Centre
301 Grant Street, 14th Floor
Pittsburgh, PA 15219-1471

Danica Masson

COPY

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PATENT
Attorney Docket No.: DB000575-023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Keeth, et al.)	Examiner: Not yet assigned
Serial No.:	Not yet assigned)	Art Unit: Not yet assigned
Filed:	22 August 2001)	
Entitled:	256 MEG DYNAMIC RANDOM ACCESS MEMORY		

PRELIMINARY AMENDMENT

Preliminary to the examination of the above-identified application filed herewith,
please amend that application as follows.

In the Specification

Page 1, line 2, after the title, insert — This Application is a divisional application of
U. S. Application Serial No. 09/621,560 filed July 21, 2000, which is a divisional application
of U.S. Application Serial No. 08/916,692 filed August 22, 1997.—

Page 52, last full paragraph, please amend as follows:

Reference is hereby made to an appendix which contains [nine microfiche having a
total of fifty-two frames] eleven microfiche having a total of sixty-six frames. The appendix
contains [33] 44 drawings which illustrate substantially the same information as is shown in
FIGs. 1-113, but in a more connected format.

A replacement page 52 is enclosed; a copy of page 52 marked to show the changes is
also enclosed.



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In the claims

Please cancel claims 1-173.

STATEMENT REQUESTING DELETION OF INVENTORS

The declaration that is being filed with the instant application is a copy of the declaration that was filed with U.S. Application Serial No. 08/916,692 filed August 22, 1997. As a result of the restriction requirement in U.S. Application Serial No. 08/916,692, the following inventors named in the grandparent application are not inventors of the invention claimed in the instant divisional application:

Raymond J. Beffa
Frank K. Ross
Larry D. Kinsman
Ronald L. Taylor
John S. Mullin

Please file the instant application in the names of the remaining inventors (Keeth, Bunker and Derner) in accordance with 37 CFR 1.63 (d).

CHANGE OF ADDRESS

Please note that the undersigned attorney's address has changed since the parent application was filed and that the undersigned attorney's address is correctly noted on form PTO/SB/05 filed herewith, and is correctly noted below.

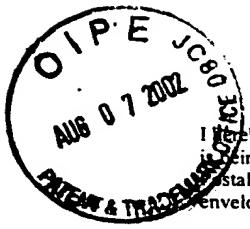
It is respectfully requested that the instant application, covering claims 174-175, receive an early office action on the merits.

Respectfully submitted

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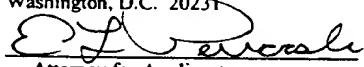
Dated: 22 August 2001

Attorneys for Applicants



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Washington, D.C. 20231


Attorney for Applicants

Date: 7 Nov. 2001

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PATENT
Attorney Docket No.: DB000575-023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Keeth, et al.)
Serial No.: 09/934,795) **Examiner:** Not yet assigned
Filed: 22 August 2001) **Art Unit:** 2816
Entitled: 256 MEG DYNAMIC RANDOM ACCESS MEMORY

SECOND PRELIMINARY AMENDMENT

Preliminary to the examination of the above-identified application, please amend that application as follows.

In the claims

Please amend claims 174 and 175 as follows.

174. (Amended) A method of controlling the conduction of a pair of isolation transistors in a sense amplifier responsive to an array, comprising [the steps of]:

rendering the pair of transistors conductive with a control signal that is a boosted version of the voltage used by the array; and

rendering the pair of transistors nonconductive by removing said control signal.

175. (Amended) The method of claim 174 wherein said step of rendering the pair of transistors conductive includes the step of rendering the transistors conductive with a control signal that is approximately a [V_t] V_{th} higher than the voltage used by the array.



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Please add the following new claims.

176. The method of claim 174 wherein said step of rendering the transistors conductive includes the step of rendering the transistors conductive with a control signal that enables the full voltage representative of a logic level one to be written to the array.

177. A method of controlling the conduction of at least one isolation transistor in a sense amplifier responsive to an array, comprising:

rendering the transistor conductive with a control signal that enables a full V_{cc} to be conducted by the isolation transistor; and

rendering the transistor nonconductive by removing said control signal.

178. The method of claim 177 wherein said step of rendering the transistor conductive includes the step of rendering the transistor conductive with a control signal that is approximately a V_{th} higher than V_{cc} .

179. A method of enabling a write to a memory array of the full voltage representative of a logic level one using a sense amplifier in which the sense amplifiers are located inside the isolation transistors, comprising:

rendering the isolation transistors conductive with a control signal that compensates for the voltage drop across the isolation transistors.

180. The method of claim 179 wherein said rendering step includes the step of rendering the isolation transistors conductive with a control signal that is approximately a V_{th} higher than the voltage used to represent a logic level one.

181. The method of claim 180 wherein said control signal is approximately V_{th} plus V_{cc} .

REMARKS

New claims 176 – 181 are presented for examination. The claims are supported by, for example, FIG. 6C and the text in the paragraph bridging pages 62 and 63. No new matter has been entered.

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STATEMENT REQUESTING DELETION OF INVENTOR

As a result of the prosecution of this application, the following individual is no longer an inventor of the subject matter being claimed and should be removed as an inventor:

Scot J. Derner

Please allow the instant application to proceed in the names of the remaining inventors (Keeth and Bunker) in accordance with 37 CFR 1.63 (d).

It is respectfully requested that the instant application, covering claims 174 – 181 receive an early office action on the merits.

Respectfully submitted



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Attorneys for Applicants

Dated: 7 November 2001

PATENT
Attorney Docket No.: DB000575-023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Keeth, et al.)
Serial No.: 09/934,795)
Filed: 22 August 2001)
Entitled: 256 MEG DYNAMIC RANDOM ACCESS MEMORY)
)
Examiner: Not yet assigned
Art Unit: 2816

COMPLETE CLEAN SET OF PENDING CLAIMS

174. (Amended) A method of controlling the conduction of a pair of isolation transistors in a sense amplifier responsive to an array, comprising:

rendering the pair of transistors conductive with a control signal that is a boosted version of the voltage used by the array; and

rendering the pair of transistors nonconductive by removing said control signal.

175. (Amended) The method of claim 174 wherein said step of rendering the pair of transistors conductive includes the step of rendering the transistors conductive with a control signal that is approximately a V_{th} higher than the voltage used by the array.

176. The method of claim 174 wherein said step of rendering the transistors conductive includes the step of rendering the transistors conductive with a control signal that enables the full voltage representative of a logic level one to be written to the array.

177. A method of controlling the conduction of at least one isolation transistor in a sense amplifier responsive to an array, comprising:

rendering the transistor conductive with a control signal that enables a full V_{CC} to be conducted by the isolation transistor; and

rendering the transistor nonconductive by removing said control signal.

178. The method of claim 177 wherein said step of rendering the transistor conductive includes the step of rendering the transistor conductive with a control signal that is approximately a V_{th} higher than V_{cc} .

179. A method of enabling a write to a memory array of the full voltage representative of a logic level one using a sense amplifier in which the sense amplifiers are located inside the isolation transistors, comprising:

rendering the isolation transistors conductive with a control signal that compensates for the voltage drop across the isolation transistors.

180. The method of claim 179 wherein said rendering step includes the step of rendering the isolation transistors conductive with a control signal that is approximately a V_{th} higher than the voltage used to represent a logic level one.

181. The method of claim 180 wherein said control signal is approximately V_{th} plus V_{cc} .



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COMMISSIONER FOR PATENTS
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/934,795	08/22/2001	2816	840	DB000575-023	367	2	1

CONFIRMATION NO. 9430

UPDATED FILING RECEIPT

OC000000008326605

Edward L. Pencoske
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One Oxford Centre
301 Grant Street, 14th Floor
Pittsburgh, PA 15219-1425

Date Mailed: 06/20/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Brent Keeth, Boise, ID;
Layne G. Bunker, Boise, ID;
Scott J. Derner, Meridian, ID;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A DIV OF 09/621,560 07/21/2000
WHICH IS A DIV OF 08/916,692 08/22/1997 PAT 6,314,011
WHICH CLAIMS BENEFIT OF 60/050,929 05/30/1997

Foreign Applications

If Required, Foreign Filing License Granted 09/25/2001

Projected Publication Date: 09/26/2002

Non-Publication Request: No

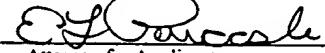
Early Publication Request: No

Title



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E. G. Garske
Attorney for Applicants

Date: 9 Oct, 2001

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ATTORNEY DOCKET NO.: DB000575-1823

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Keeth, et al)	Examiner:	Not yet assigned
Serial No.:	09/934,795)	Art Unit:	2816
Filed:	22 August 2001)		
Entitled:	256 MEG DYNAMIC RANDOM ACCESS MEMORY			

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
United States Patent and Trademark Office
Office of Initial Patent Examination
Customer Service Center
Washington, DC 20231

Dear Sir:

Applicants hereby request a Corrected Filing Receipt be issued for the above-referenced patent application. The Patent Office has incorrectly listed the Applicants for this invention.

Applicants respectfully request that the Applicants be listed to include only Brent Keeth, Layne G. Bunker, and Scott J. Dermer. The Statement Requesting Deletion of Inventors (copy enclosed), which was included in the Preliminary Amendment that was filed in the above-identified application, requested that this application be filed only in the names of Keeth, Bunker and Dermer.

A copy of the original Filing Receipt with the corrections marked in red ink is enclosed.

Also enclosed is a return postcard. Please date stamp the postcard and return it to us to acknowledge receipt of this Request.



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The Commissioner is hereby authorized to charge any underpayment or credit any overpayment to our Deposit Account No. 20-0888. A duplicate copy of this Transmittal letter is enclosed.

Respectfully submitted,

Edward L. Pencoske
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Dated: 9 October 2001

Attorney for Applicants

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL. FEE RECD	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/934,795	08/22/2001	2816	710	DB000575-023	367	2	1

CONFIRMATION NO. 9430

FILING RECEIPT



OC00000006792316

Edward L. Pencoske
Throp Reed & Armstrong LLP
One Oxford Centre
301 Grant Street
Pittsburgh, PA 15219-1425

Date Mailed: 09/26/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Brent Keeth, Boise, ID;
Layne G. Bunker, Boise, ID;
Scott J. Derner, Meridian, ID;
Ronald L. Taylor, Meridian, ID;
John S. Mullin, Boise, ID;
Raymond J. Beffa, Boise, ID;
Frank F. Ross, Boise, ID;
Larry D. Kinsman, Boise, ID;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A DIV OF 09/621,560 07/21/2000
WHICH IS A DIV OF 08/916,692 08/22/1997
WHICH CLAIMS BENEFIT OF 60/050,929 05/30/1997

Foreign Applications

If Required, Foreign Filing License Granted 09/25/2001

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

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Title

256 meg dynamic random access memory

Preliminary Class

327

**LICENSE FOR FOREIGN FILING UNDER
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Title 37, Code of Federal Regulations, 5.11 & 5.15**

GRANTED

The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Office of Export Administration, Department of Commerce (15 CFR 370.10 (j)); the Office of Foreign Assets Control, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

NOT GRANTED

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).